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ON Semiconductor
5005 East McDowell Road
Phoenix, AZ 85008
<http://onsemi.com>

Formerly a Division of Motorola

*Law Department
Mail Drop A700*

Writer's Direct Line: 602-244-5226

March 30, 2000

VIA CERTIFIED MAIL

Top Hat Plan Exemption
Pension and Welfare Benefits Administration
Room N-5638
U.S. Department of Labor
200 Constitution Avenue, N.W.
Washington, D.C. 20210

Re: Semiconductor Components Industries, LLC, dba ON Semiconductor

Dear Sir or Madam:

Enclosed for filing with the Secretary of Labor is a statement satisfying the alternate form of compliance with the reporting and disclosure requirements of Part 1 of Title 1 of the Employee Retirement Income Security Act of 1974 ("ERISA"). This statement is intended to fulfill the requirements set forth in Department of Labor Regulation Section 2520.104-23 with respect to the ON Semiconductor Amended and Restated Executive Deferred Compensation Plan that Semiconductor Components Industries, LLC established effective December 13, 1999, for the benefit of certain of its employees who are members of a "top-hat group."

Very truly yours,

Semiconductor Components Industries, LLC

A handwritten signature in black ink, appearing to read "George H. Cave".

George H. Cave
Secretary

Enclosure

SEMICONDUCTOR COMPONENTS INDUSTRIES, LLC

**STATEMENT PURSUANT TO DEPARTMENT OF LABOR
REGULATIONS SECTION 2520.104-23**

Semiconductor Components Industries, LLC (SCI LLC), dba ON Semiconductor (the "Company") hereby submits the following information to the Secretary of Labor for the ON Semiconductor Amended and Restated Executive Deferred Compensation Plan ("Plan") that the Company established effective December 13, 1999, for certain of its employees who are part of a top-hat group.

1. Name and address of employer:

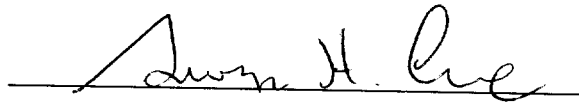
Semiconductor Components Industries, LLC
5005 E. McDowell Road
Phoenix, Arizona 85008

2. Employer identification number assigned by the Internal Revenue:

36-4292817

3. The Company has established the Plan primarily for the purpose of providing deferred compensation for a select group of management or highly compensated employees.
4. At this time, the ON Semiconductor Amended and Restated Executive Deferred Compensation Plan is the only plan sponsored by the Company that, as its primary purpose, provides deferred compensation for a select group of management or highly compensated employees. The Company anticipates that approximately 90 employees will participate in the plan.

Semiconductor Components Industries, LLC,
dba ON Semiconductor



By: George H. Cave

Its: Secretary

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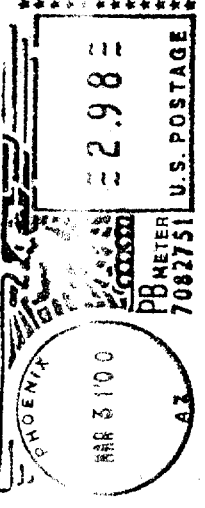


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the right of the return address

CERTIFIED

Z 371 655 652

MAIL



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